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SPICE OPUS is a free general purpose circuit simulator specially suited for optimization loops. It is a recompilation of the original Berkeley source code for Windows and Linux operating systems. Later Georgia Tech Research Institute's XSpice mixed-mode simulator was added to the Berkeley code. The XSpice code model feature was enhanced so that code models can be loaded from dll/so files (.cm files).

SPICE OPUS

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SPICE OPUS is a circuit simulator

available in two versions: (i) SPICE OPUS Lite; and (ii) SPICE OPUS Pro. It is a recompilation of the original Berkeley ’ s source code for Windows 95/98/NT and Linux operating systems with Georgia Tech Research Institute ’ s XSPICE mixed-mode simulator.

~~A Simplied Introduction to Circuit Simulation using SPICE OPUS~~
SPICE OPUS is the perfect tool for the latter. The simulation examples in this chapter have been selected to emphasize the unique scripting capabilities of SPICE OPUS employed to speed up the...

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SPICE OPUS does not have a built-in schematic program. However, we can use an external schematic program called EAGLE to prepare the circuit schematic. But using a text also helps in better understanding of the basics of circuit simulation. So we will use *.cir to describe the circuit.

~~ANALOG SIMULATION INTRODUCTION USING SPICE OPUS~~

Title: Circuit Simulation with SPICE OPUS, Theory and Practice Series: Modeling and Simulation in Science, Engineering and Technology Authors: Tadej Tuma, Árpád

B û rmen 2009, Approx. 480 p. 158 illus., Hardcover ISBN: 978-0-8176-4866-4, A Birkh ä user book Download simulation examples from chapter 7 (examples07.zip 23KB)

SPICE OPUS

SPICE (" Simulation Program with Integrated Circuit

Emphasis ") is a general-purpose, open-source analog electronic circuit simulator . It is a program used in integrated circuit and board-level design to check the integrity of circuit designs and to predict circuit behavior.

SPICE - Wikipedia
Spice Opus 20 -> dc i1 -100uA 100uA 10uA

Spice Opus 21 -> plot v(3) xlabel i1[A] ylabel v(3)[V] title 'Response of Optimal Circuit' In our case parameter space is two dimensional, so the cost function could be plotted. First the cost function has to be calculated over the entire explicitly constrained parameter space.

SPICE OPUS

Circuit Simulation with SPICE OPUS is intended for a wide audience of undergraduate and graduate students, researchers, and practitioners in electrical and systems engineering, circuit design, and simulation development. The book may be used as a textbook for an advanced undergraduate or graduate course on circuit simulation as well as a self-study reference guide for students and researchers alike.

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SPICE OPUS is an improved version of SPICE based on the original SPICE 3f5 code with extensions for circuit and device performance optimization and a transient simulation shooting method for large signal steady state AC analysis. SPICE OPUS can be downloaded from its official website at http://www.spiceopus.si/.

~~Basic Ngspice, Xyce and SPICE OPUS simulation — Qucs-S ...~~
Circuit Simulation with SPICE OPUS is intended for a wide audience of

undergraduate and graduate students, researchers, and practitioners in electrical and systems engineering, circuit design, and simulation development.

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~~Circuit Simulation with SPICE OPUS - CORE~~

SPICE OPUS comes with a precompiled set of CMs and UDNs. Among those you can find integer and real

UDNs, blocks for system level analog simulation, various digital gates and node bridges. CMs and UDNs are stored in a.cm file that can be loaded into the simulator. In order to load a.cm file, type the following command in the NUTMEG prompt: